APR 2 6 2006

AGILENT TECHNOLOGIES, INC. Legal Department, DL429 Intellectual Property Administration P. O. Box 7599 Loveland, Colorado 80537-0599

4348232242

ATTORNEY DOCKET NO. 10031350-1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Hildebrant

Serial No.: 1/815,521

Examiner: Doan, Nghla M

Filing Date: April 1, 2004

Group Art Unit: 2825

Title: VERIFICATION OF INTEGRATED CIRCUIT TESTS USING TEST SIMULATION AND

INTEGRATED CIRCUIT SIMULATION WITH SIMULATED FAILURE

COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria VA 22313-1450

TRANSMITTAL LETTER FOR RESPONSE/AMENDMENT

Sir	:					_14,					
Tra	nsmitted h	erewith is/are the follo	wing in the	above-identified app	lication:						
×	Response/Amendment				Petition to extend time to respond						
	New fee as calculated below				Supplemental Declaration						
×	No additi	onal fee (Address e	nvelope to	"Mail Stop Amendme	ents")						
	Other:				(Fee \$)					
		CLAIMS	AS AMENDE	D BY OTHER THAN A S	MALL ENTITY						
	(1) FOR	(2) CLAIMS REMAINING AFTER AMENDMENT	(3) NUMBER EXTRA	(4) HIGHEST NUMBER PRÉVIOUSLY PAID FO	(5) PRESENT R EXTRA	(6) RATE	ADD	(7) FIONAL EES			
	TOTAL CLAIMS	32	MINUS	37	= 0	X 50	\$	0			
	INDEP.	3	MINUS	3	= 0	X 200	\$	0			

FIRST PRESE	NTATION OF A MUL	TIPLE DEPENDENT	CLAIM			+3	60	\$ 0
EXTENSION FEE	1 ST MONTH 120.00	2 ND MONTH 450.00	3 ND MONTH 1020.00 ☐		4 ^{1H} M 1590.		5	\$ 0
					OTI	IER	FEES	\$ 0
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT								\$ 0

any fees required or credit any over payment to Deposit Account 50-1078 pursuant to 37 CFR 1.2 5. Additionally please charge any fees to Deposit Account 60-1078 under 37 CFR 1.16, 1.17, 1.19, 1.20 and 1.21. A duplicate copy of this transmittal letter is enclosed.

I hereby certify that this paper is being facsimile transmitted to the Patent and Trademark Office on the date shown below:

Date of facsimile: 4-26-2008

Typed Name: Jessica Costa

Respectfully submitted,

Hildebrant

Jessica Costa

Attomey/Agent for Applicant(s)

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Rev 09/05 (TronsAma)

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APR 2 6 2006.

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

PATENT APPLICATION: Serial No. 10/815,521

ORIGINALLY FILED: April 1, 2004

FOR:

VERIFICATION OF INTEGRATED CIRCUIT TESTS USING TEST SIMULATION AND INTEGRATED CIRCUIT SIMULATION WITH SIMULATED FAILURE

INVENTOR: Hildebrant, Andrew

GROUP ART UNIT: 2825

EXAMINER: Doan, Nghia M

DOCKET NUMBER: 10031350-1

AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed January 26, 2006, please amend the above-identified patent application and consider the remarks as follows:

Amendments to the Claims are reflected in the listing of claims which begin on page 2 of this paper.

Remarks/Arguments begin on page 10 of this paper.